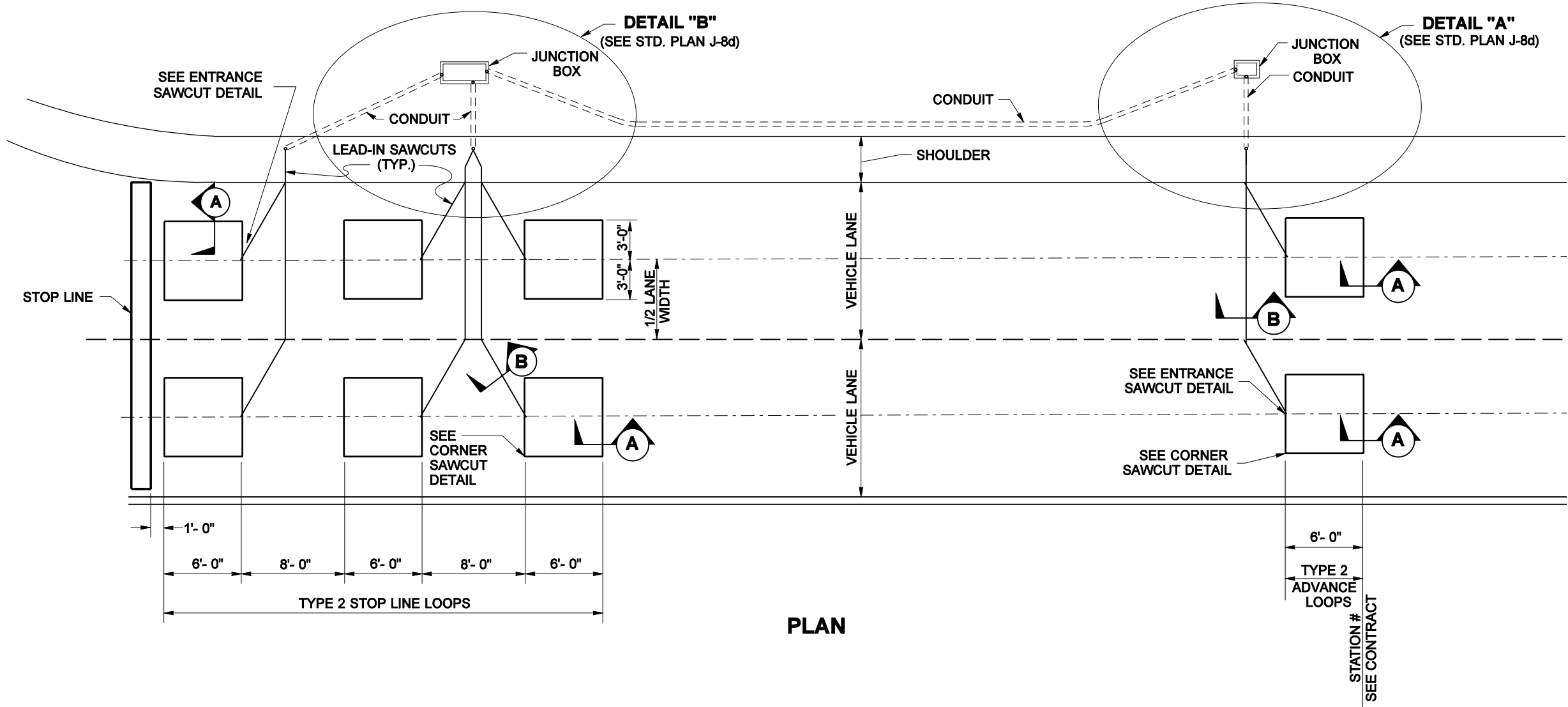


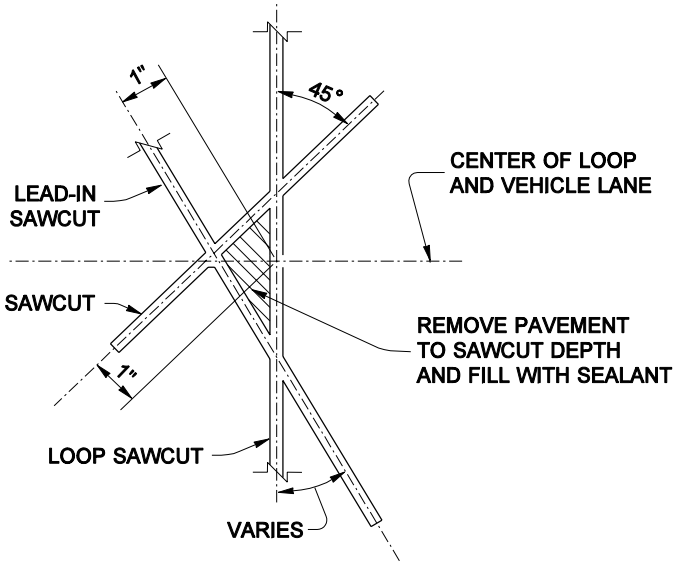
DRAWN BY: MONIQUE GLICK

NOTE: THIS PLAN IS NOT A LEGAL ENGINEERING DOCUMENT BUT AN ELECTRONIC DUPLICATE. THE ORIGINAL, SIGNED BY THE ENGINEER AND APPROVED FOR PUBLICATION, IS KEPT ON FILE AT THE WASHINGTON STATE DEPARTMENT OF TRANSPORTATION. A COPY MAY BE OBTAINED UPON REQUEST.

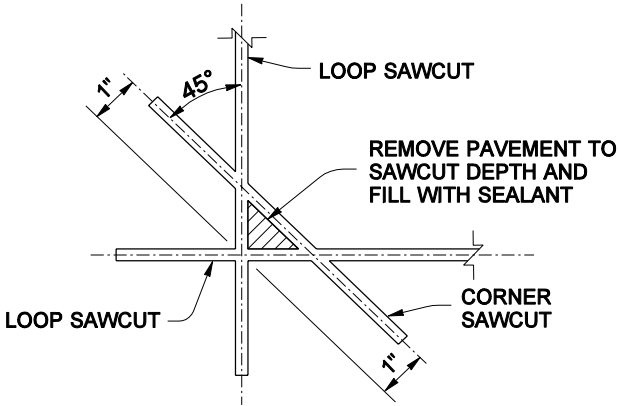


NOTE
1. For Sections A and B, see Standard Plan J-8d.

PLAN



ENTRANCE SAWCUT DETAIL



CORNER SAWCUT DETAIL



EXPIRES MAY 5, 2005

TYPE 2 INDUCTION LOOP

STANDARD PLAN J-8b

SHEET 1 OF 2 SHEETS

APPROVED FOR PUBLICATION

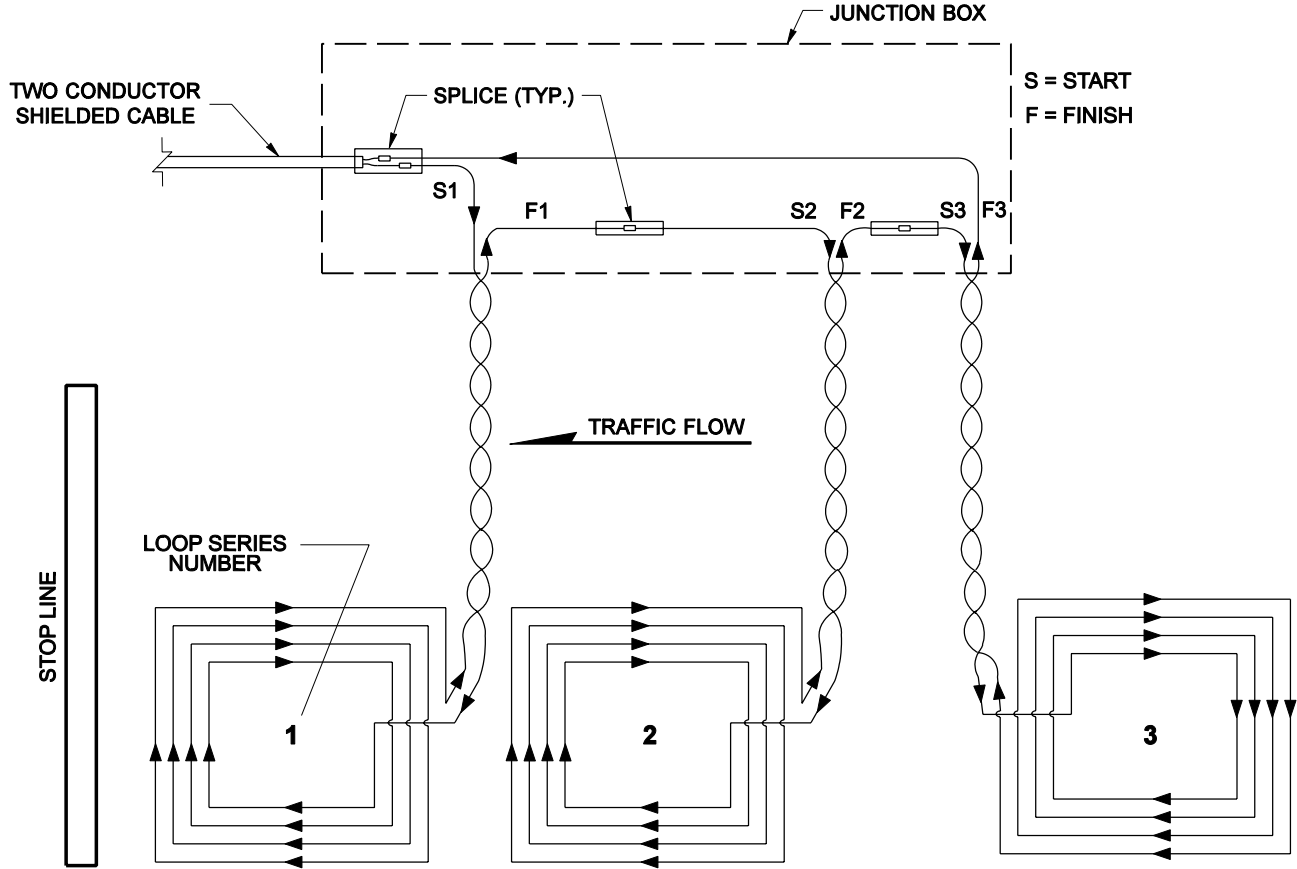
Harold J. Peterfeso 05-20-04

STATE DESIGN ENGINEER

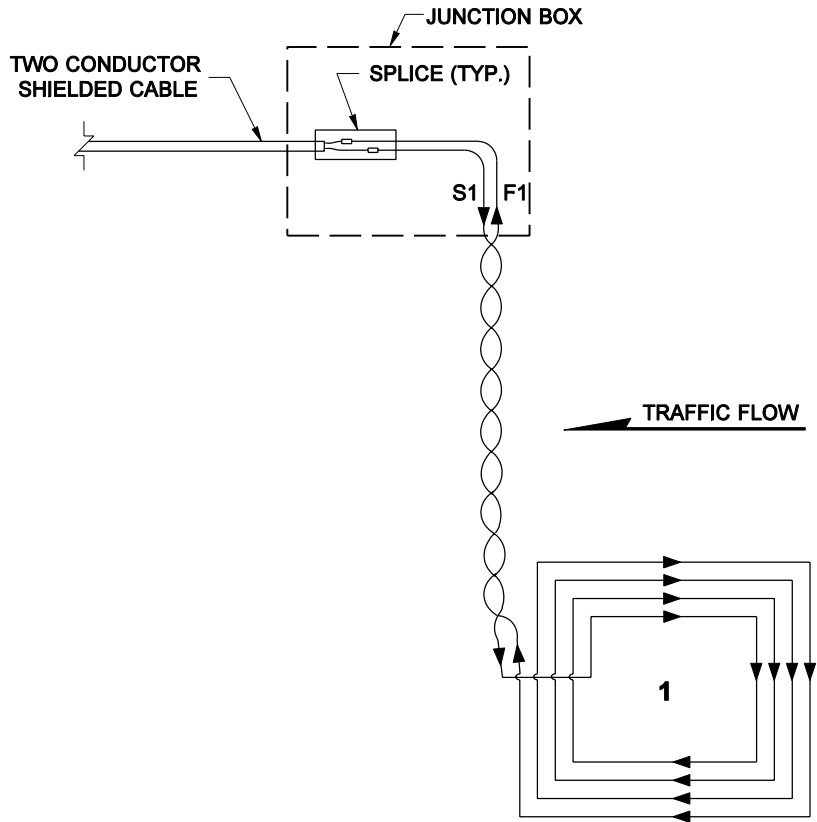
DATE



Washington State Department of Transportation



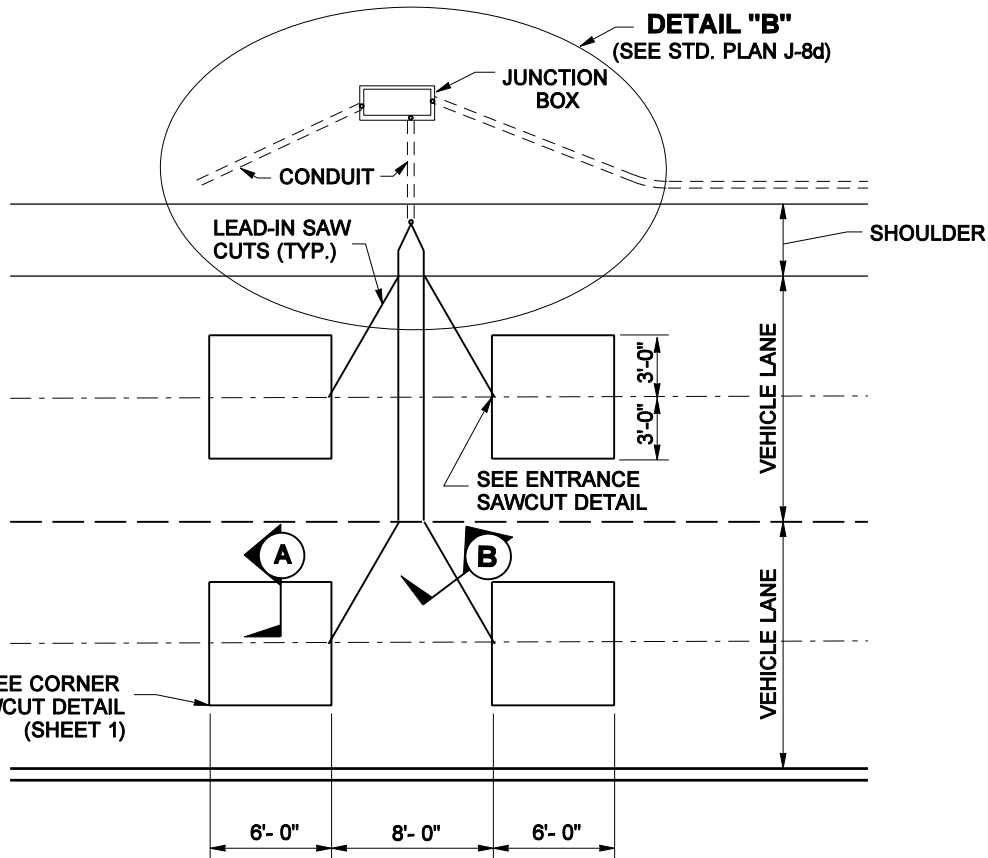
TYPE 2 STOP LINE LOOP WIRING DIAGRAM
(SERIES SPLICE SHOWN)



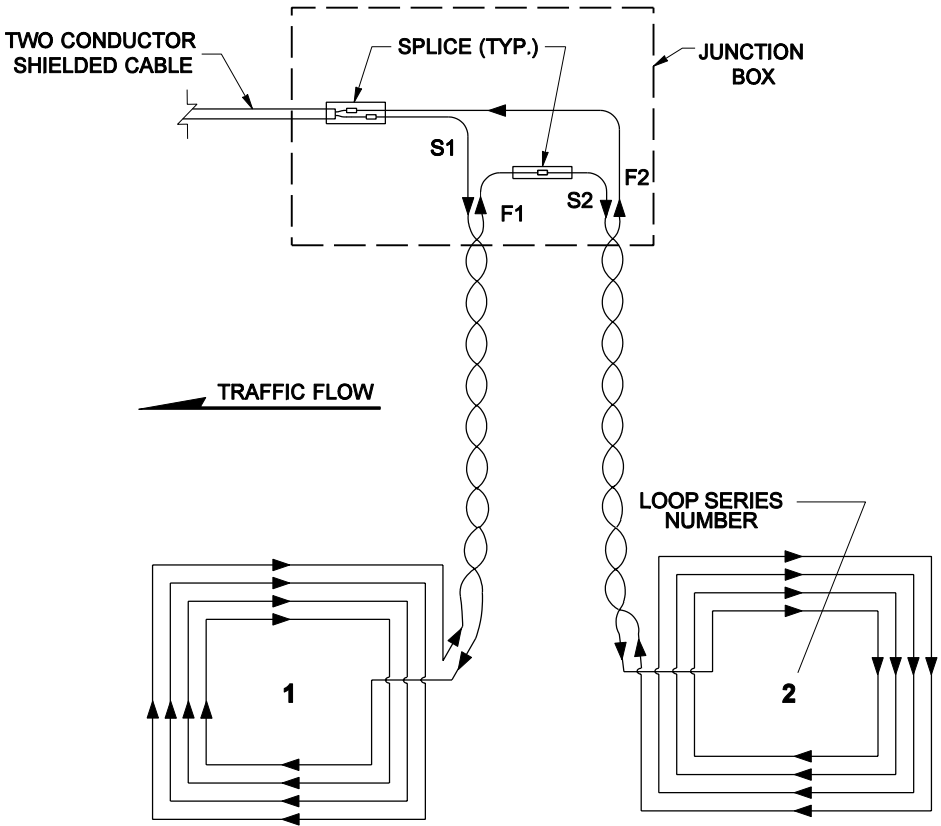
TYPE 2 ADVANCE LOOP WIRING DIAGRAM

NOTES

1. All of the loop lead-in wires shall return to the Junction Box.
2. For Splice Detail, see Standard Plan J-8d.



TYPE 2 SAMPLING LOOPS



TYPE 2 SAMPLING LOOP WIRING DIAGRAM
(SERIES SPLICE SHOWN)



EXPIRES MAY 5, 2005

TYPE 2 INDUCTION LOOP

STANDARD PLAN J-8b

SHEET 2 OF 2 SHEETS

APPROVED FOR PUBLICATION

Harold J. Peterfeso

05-20-04

STATE DESIGN ENGINEER

DATE



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